

Advanced Signal Equalization Methods for 448G

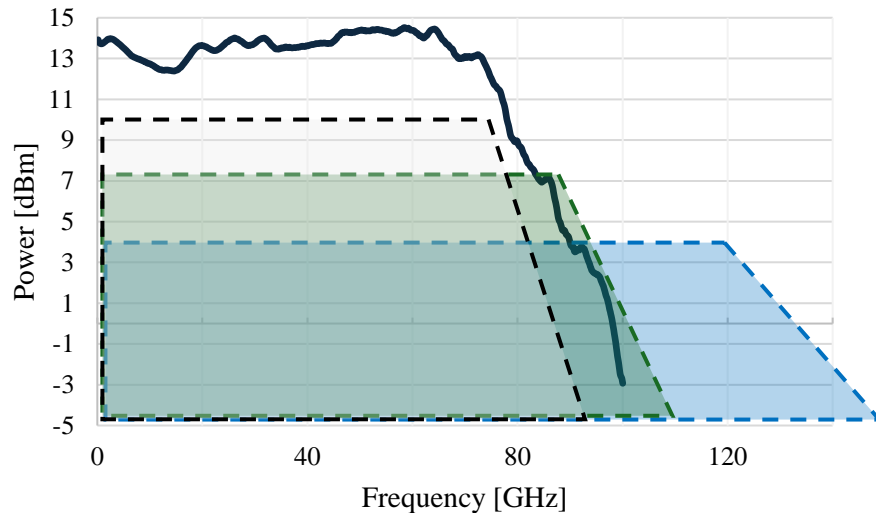
Abstract: The progression to 448G interfaces necessitates substantial enhancements in critical link components, including SerDes, packaging, channels, and connectors. Additionally, it may demand the adoption of advanced equalization and error correction techniques. This presentation evaluates the applicability of existing equalization strategies for 448G transmissions. Specifically, it examines the integration of Continuous Time Linear Equalizer (CTLE), Feed-Forward Equalizer (FFE), Decision Feedback Equalizer (DFE), and Maximum Likelihood Sequence Detection (MLSD), as initially defined in OIF-CEI-224G-LR and PIEEE 802.3dj standards for 224G interfaces, in the context of 448G links utilizing 224Gbaud PAM4 or 180Gbaud PAM6 modulation schemes. The discussion will incorporate state-of-the-art channel designs to assess the feasibility and performance of these equalization techniques in meeting the stringent requirements of 448G data transmission.

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Presenter: John Calvin (john.calvin@keysight.com)

448G Channel Considerations (M8199B/UXR Instrumentation grade setup)

Spectral Power envelope of a M8199B Arbitrary Waveform Generator



220GBd PAM4 (440Gb/s)

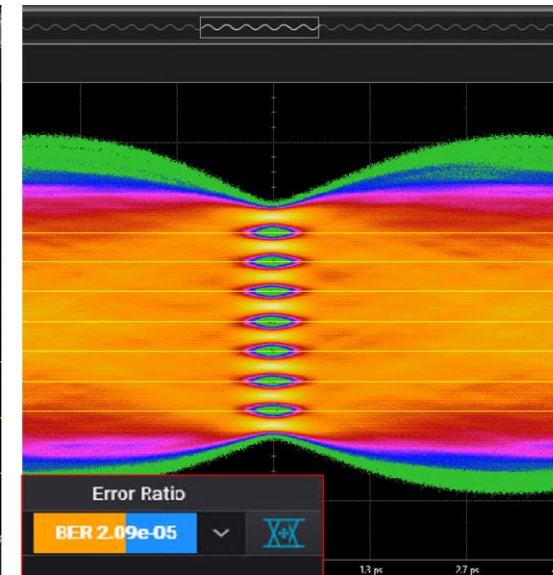
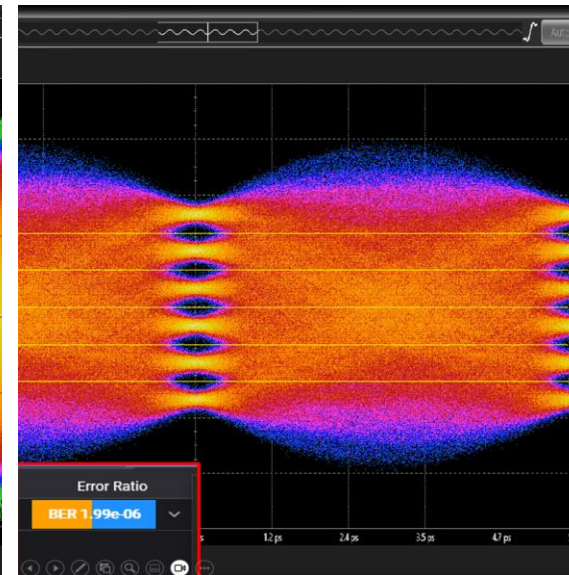
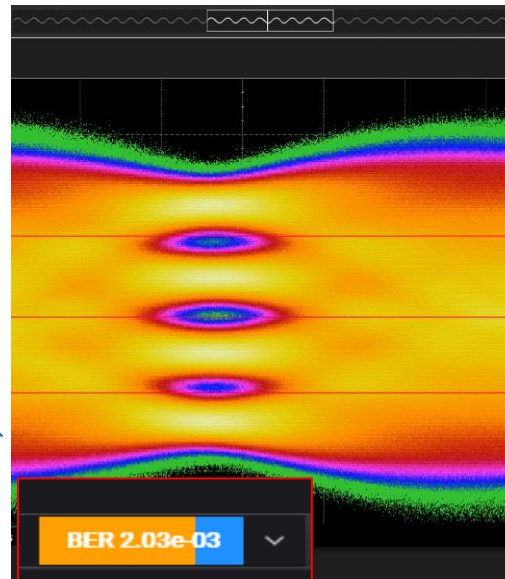
- $2e-3$ BER
- Main limitation: Non-compensable signal distortion (also impacting the CDR scheme)

176GBd PAM6 (440Gb/s)

- $2e-6$ BER
- Main limitation: SNR (Tx)

150GBd PAM8 (450Gb/s)

- $2e-5$ BER
- Main limitation: SNR (Tx & Rx)



448G Electro/Optic Requirements

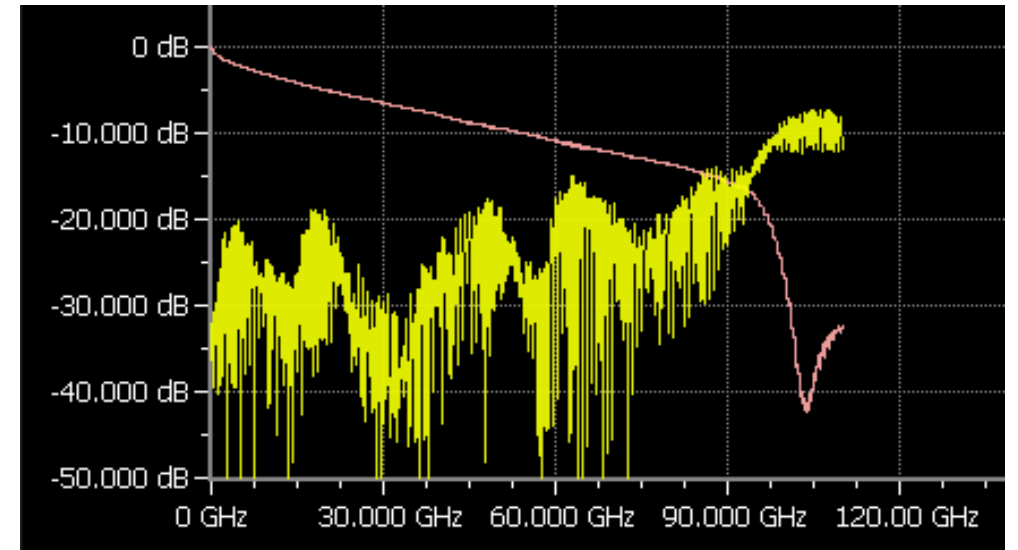
Higher-Order Modulation tradeoffs

Fractional Symbol Rate
 • Breaks conventional KP4 FEC

	PAM4	PAM6	PAM8
FEC	6.25%	6.25%	6.25%
Min. Signal Rate [Gbd]	212.5	170	141.6
Nyquist [GHz]	106.25	85	71
Δ SNR [dB] (relative PAM4)	0	3.6	6.1

Challenging for optical links

- Higher laser power
- Improved Rx sensitivity
- New FEC (power, latency)



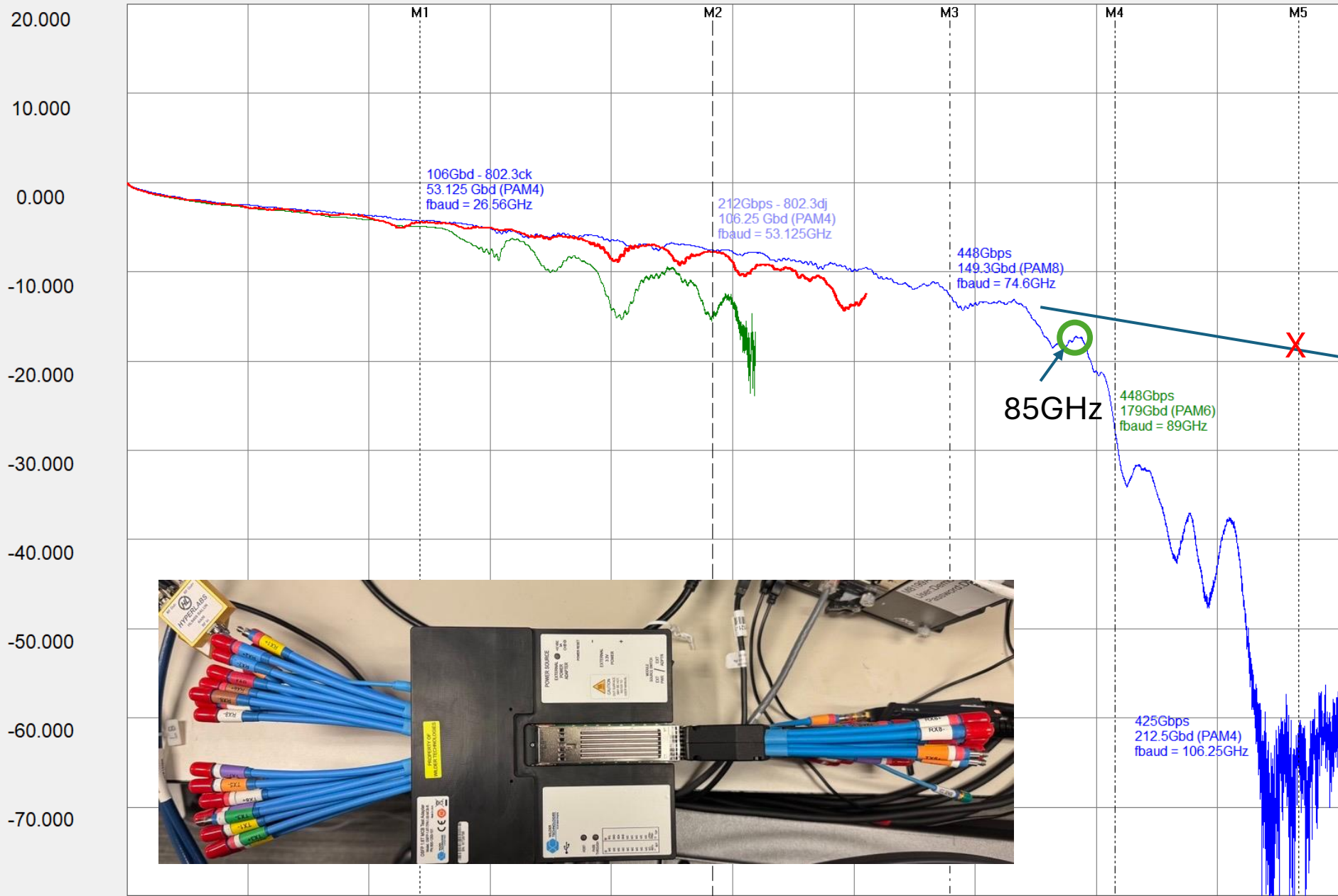
e.g. state-of-the-art 10 cm channel with compression connectors

Challenging for electrical channels

- Max frequency ~100GHz
- ERL, IL, FEXT/NEXT are real physical issues
- Need better (higher BW) connection technology

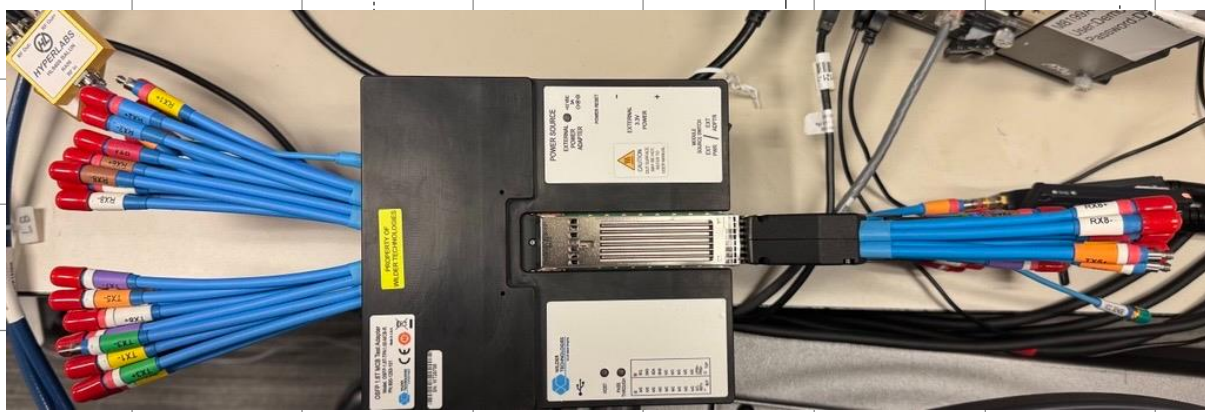
OSFP MTF (HCB+MCB) SDD21

10.000 dB/div



SDD21 OSFP (3.2T ??)
SDD21 OSFP (1.6T)
 SDD21 OSFP (800G)

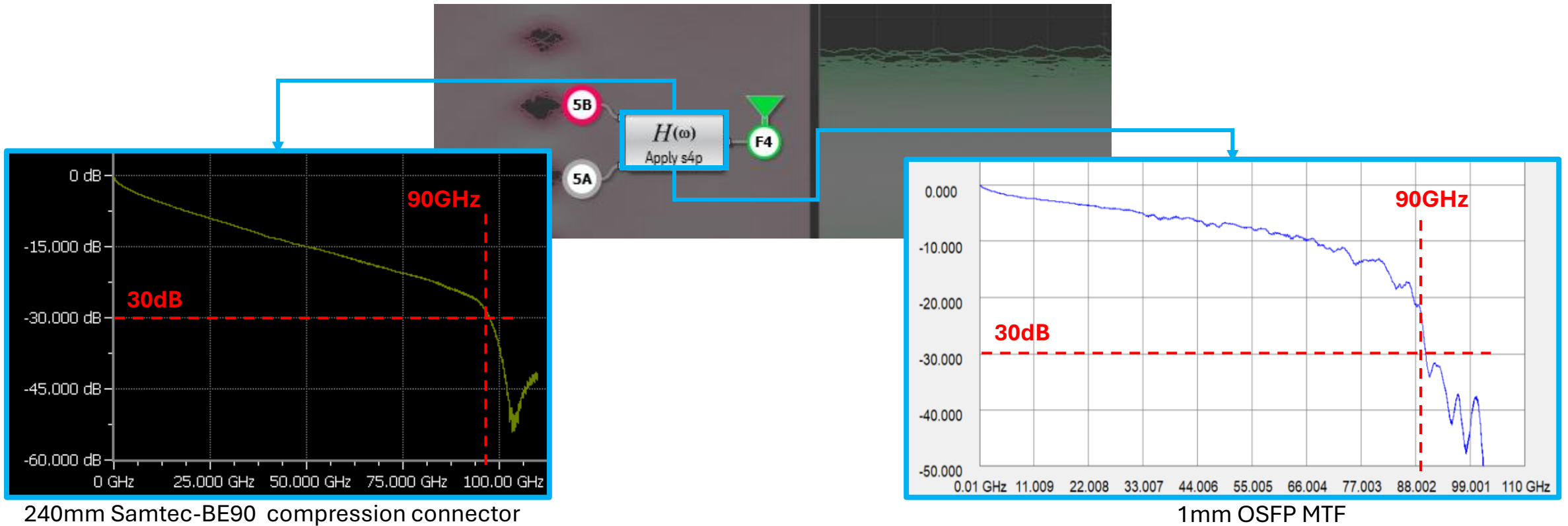
1:X	26.56 GHz
1:Y	-4.48 dB
2:X	53.13 GHz
2:Y	-7.82 dB
3:X	74.6 GHz
3:Y	-8.22 dB
4:X	89.6 GHz
4:Y	-6 dB
5:X	106.25 GHz
5:Y	-7.41 dB



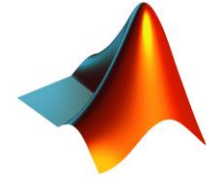
0.01 GHz 11.009 22.008 33.007 44.006 55.005 66.004 77.003 88.002 99.001 110 GHz 10.999 GHz/div

425Gb/ Electrical - Investigation

- Emulated channel



425Gb/ Electrical – Equalizer Investigation

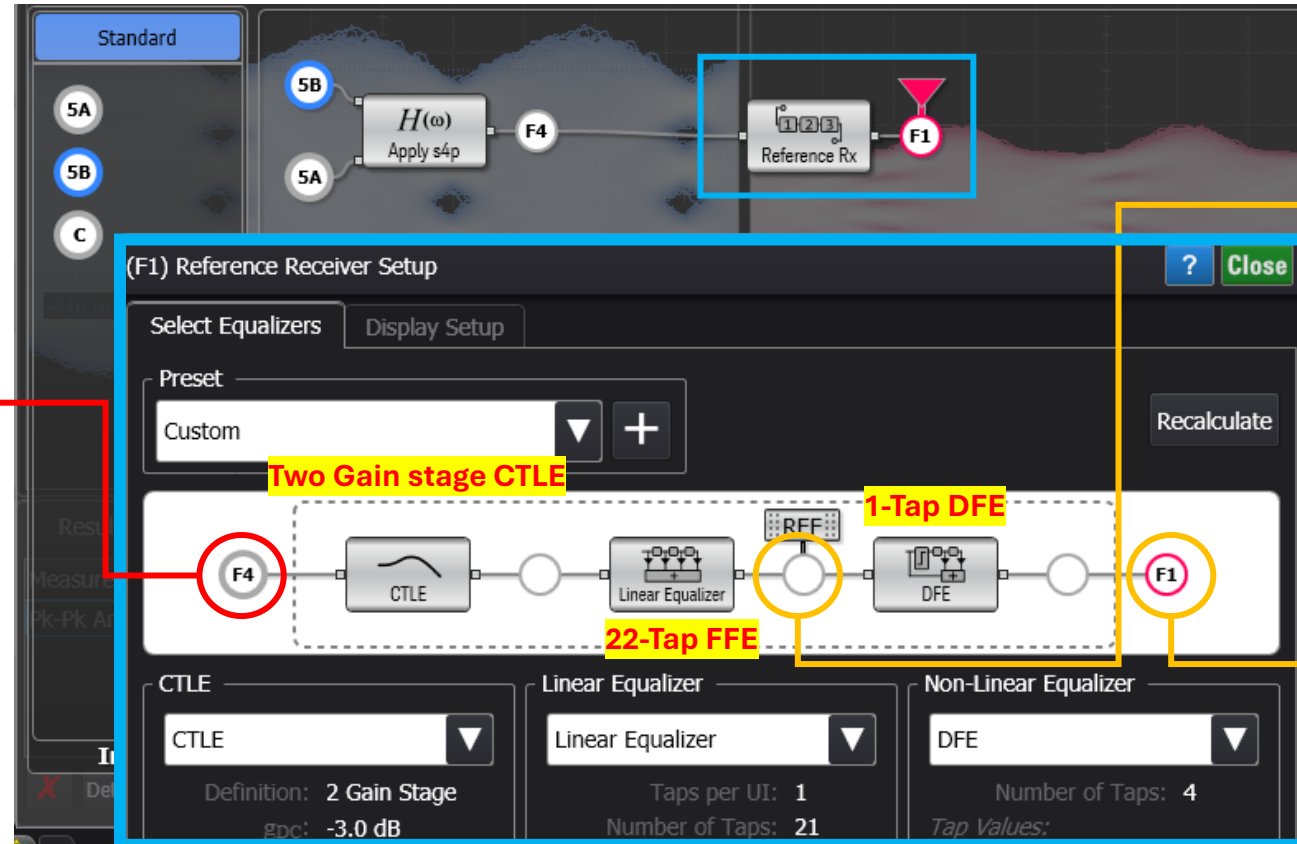


Matlab post processing

- PR-MLSD
- SER

PR-MSLD

- 15-Tap pre-shaping filter (in addition to 22-Tap FFE)
- 1-transistion memory
- Trellis length: 10 symbols



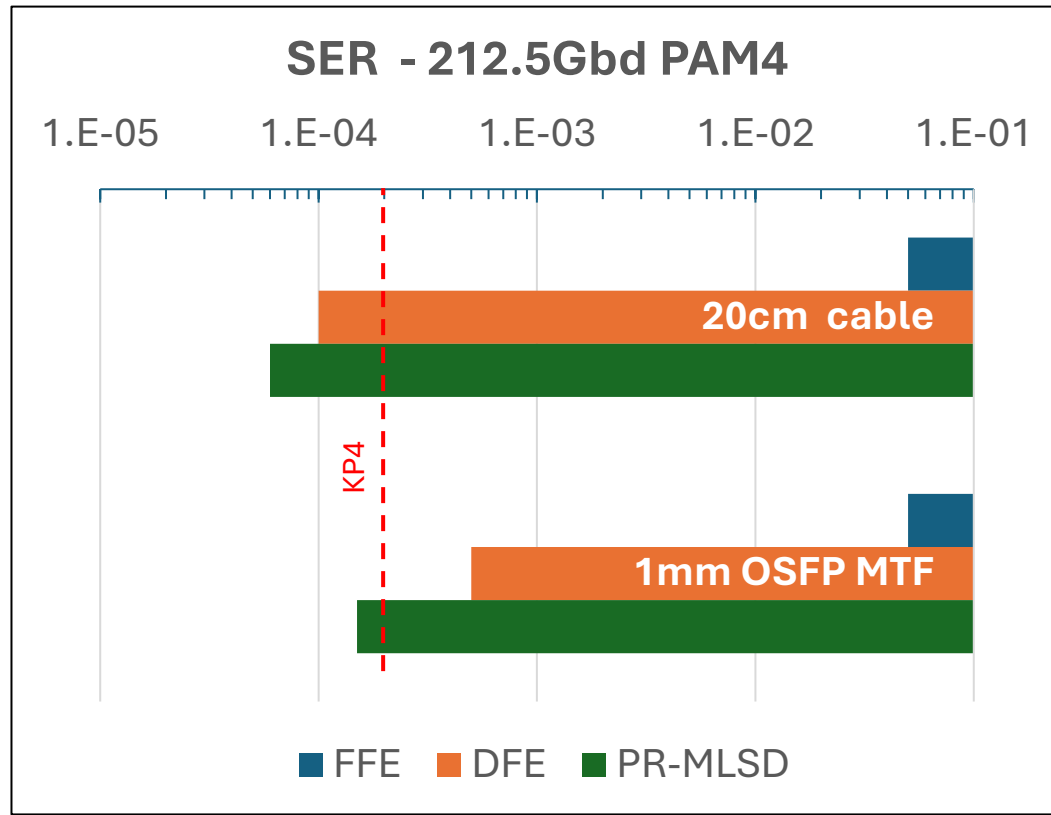
Important note

- No input referred noise ($\eta_{a_0}=0$)
- SNR does not degrade after the channel

FFE & DFE are co-optimized (pulse Response & MMSE)

425Gb/ Electrical - Investigation

- PAM4 results



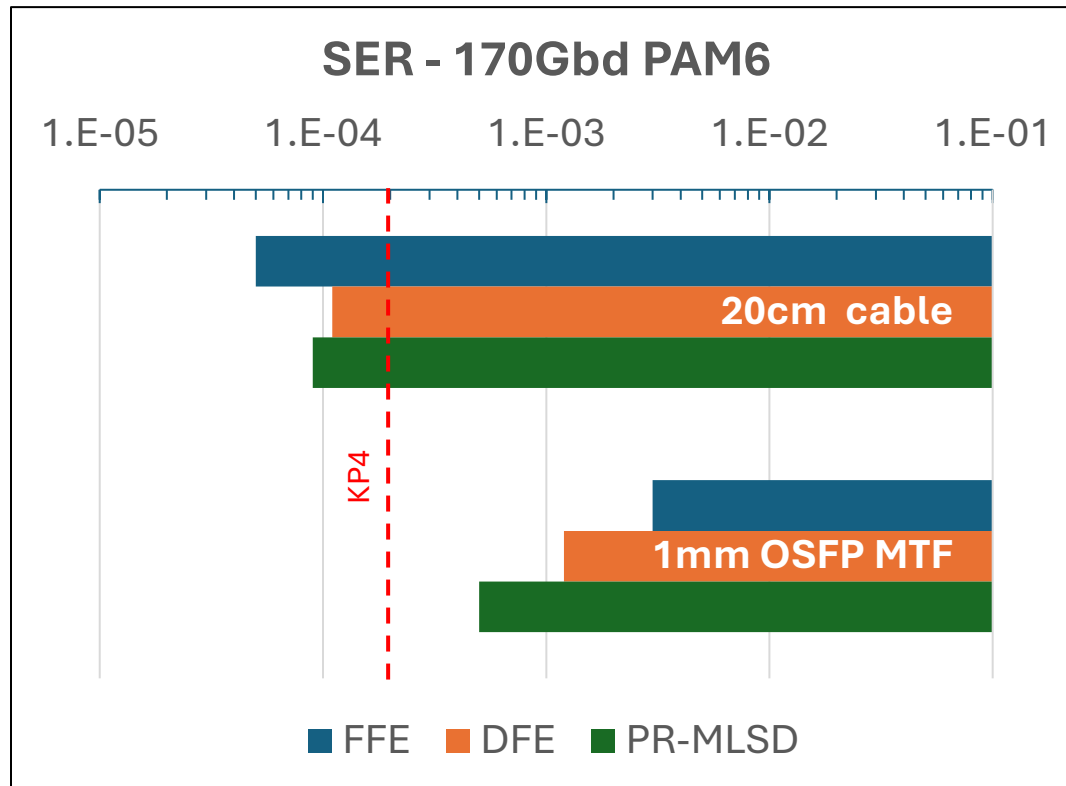
425Gb/s PAM4

- CTLE+FFE not capable of equalizing the signal
- CTLE+PR-MLSD has slightly better performance than CTLE+FFE+DFE
- Operation below KP4 FEC Threshold is possible but no margin
- Operation below 1E-5 BER not possible

→ We hitting a fundamental limitation for PAM4 (BTB SER < 1e-6)

425Gb/ Electrical - Investigation

- PAM6 results



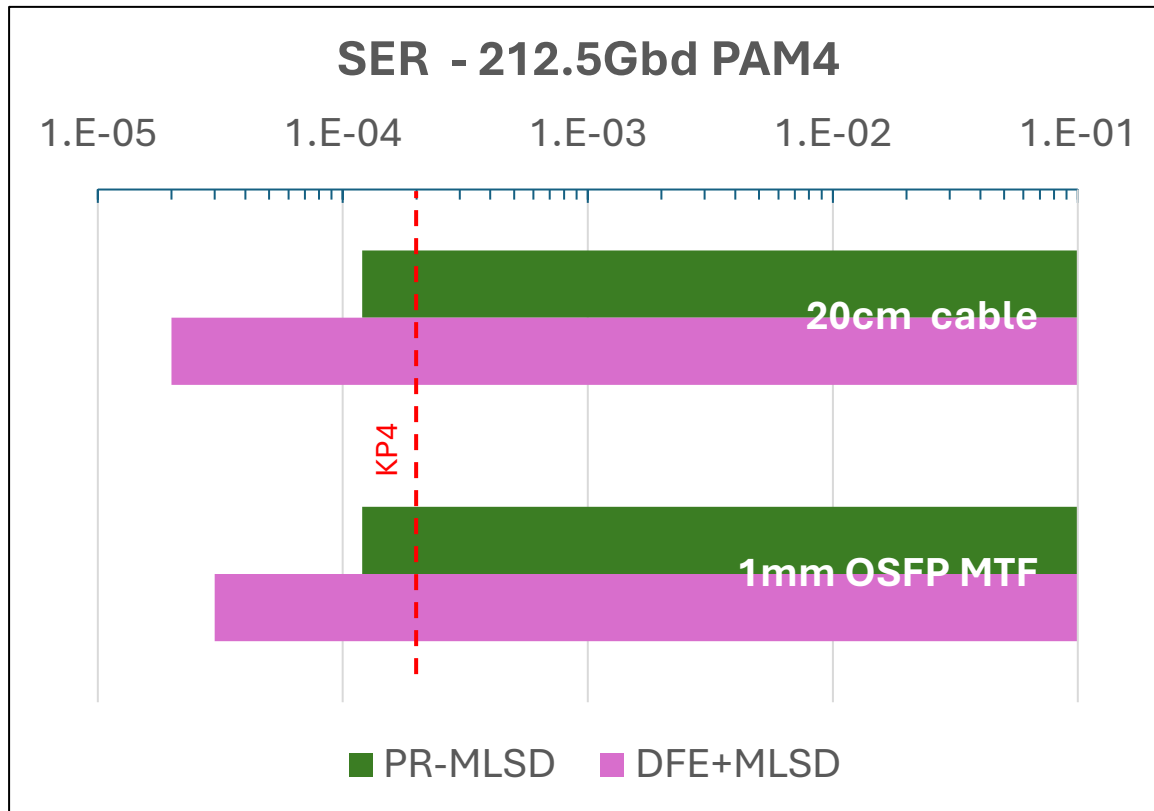
425Gb/s PAM6

- No significant improvement using DFE/MLSE vs FFE → why?
- Operation below KP4 FEC Threshold is possible only for one channel
- Operation below 1E-5 BER not possible

→ Is PAM6 link mainly limited by SNR
(BTB SER = 3e-5)

425Gb/ Electrical - Investigation

- Improvements on the equalizer side are possible



Current setting far from optimal

- CTLE not included in the optimization
- DFE tap and sampling time probably not optimum

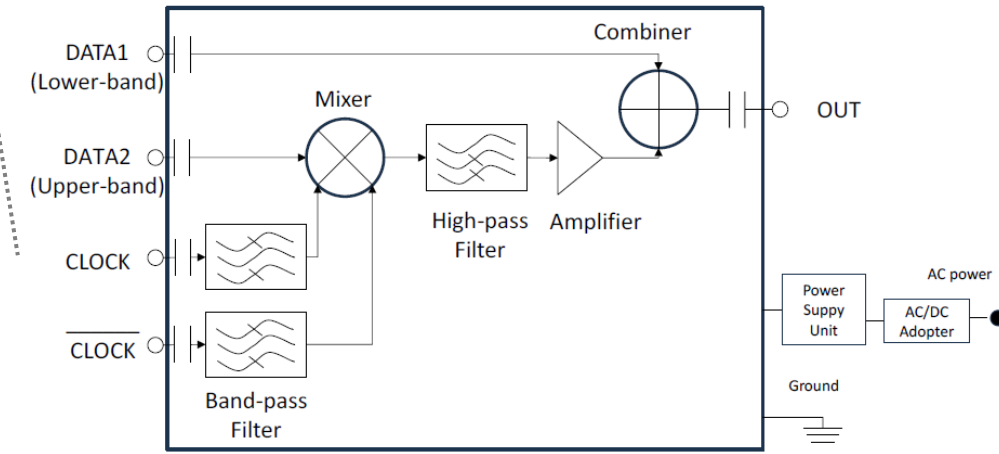
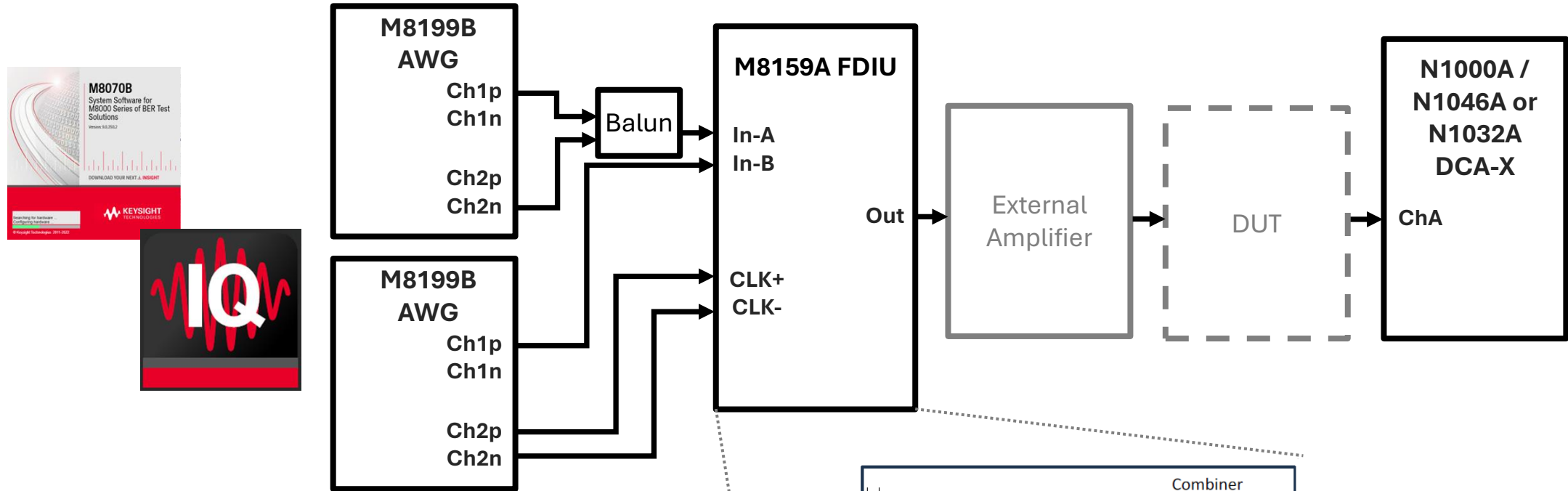
Further improvement

- Combining DFE followed by PR-MLSD enables almost one order of magnitude in BER (is MLSD mitigating DFE burst behavior, tbc)

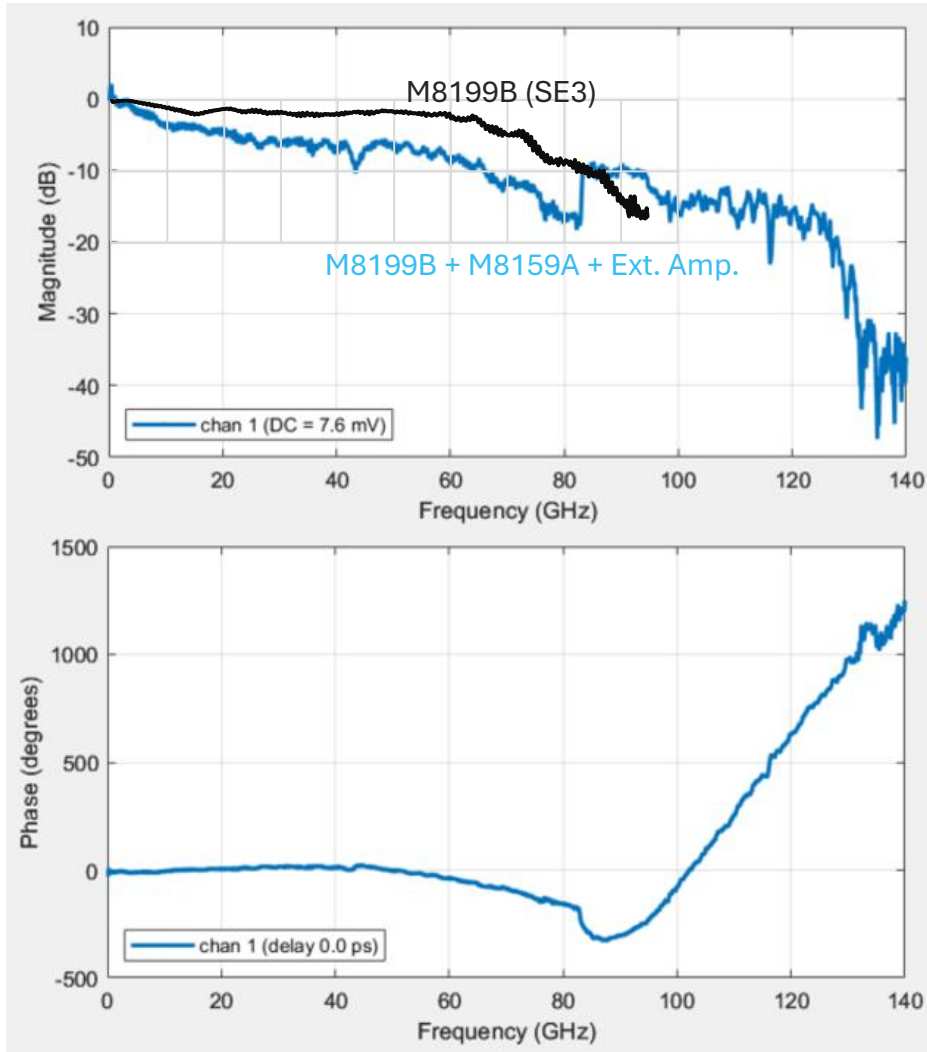
→ **Good chances that improved equalizer architectures make 425G PAM4 work for these channels**

How to extend the bandwidth of the M8199B Arbitrary Waveform Generator

Reference Setup

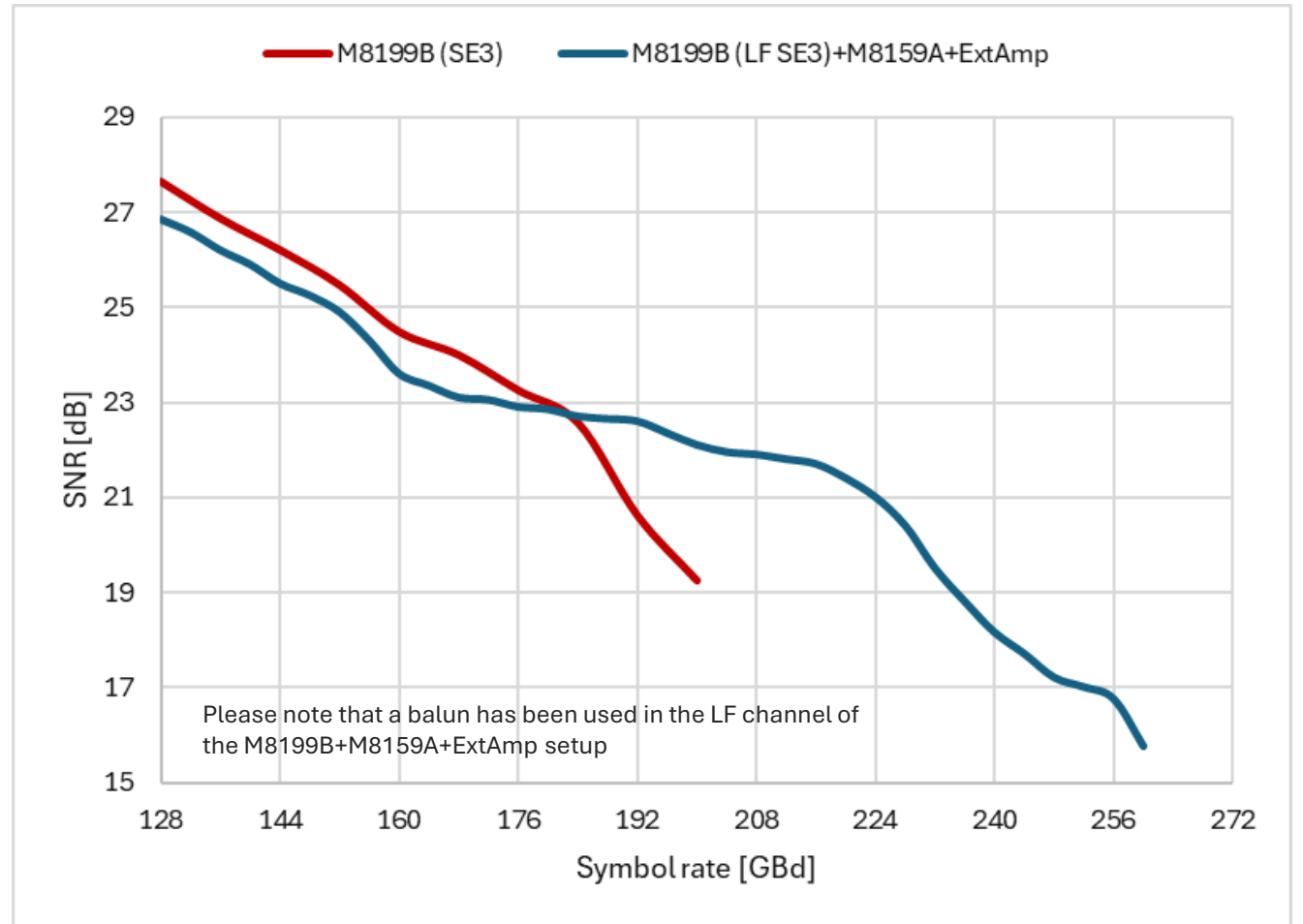


M8199B AWG + M8159A FDIU + External Amplifier



SNR @224 Gbaud: ≥ 20 dB

Output amplitude @224 Gbaud: ~ 700 mV_{se} (OMA)

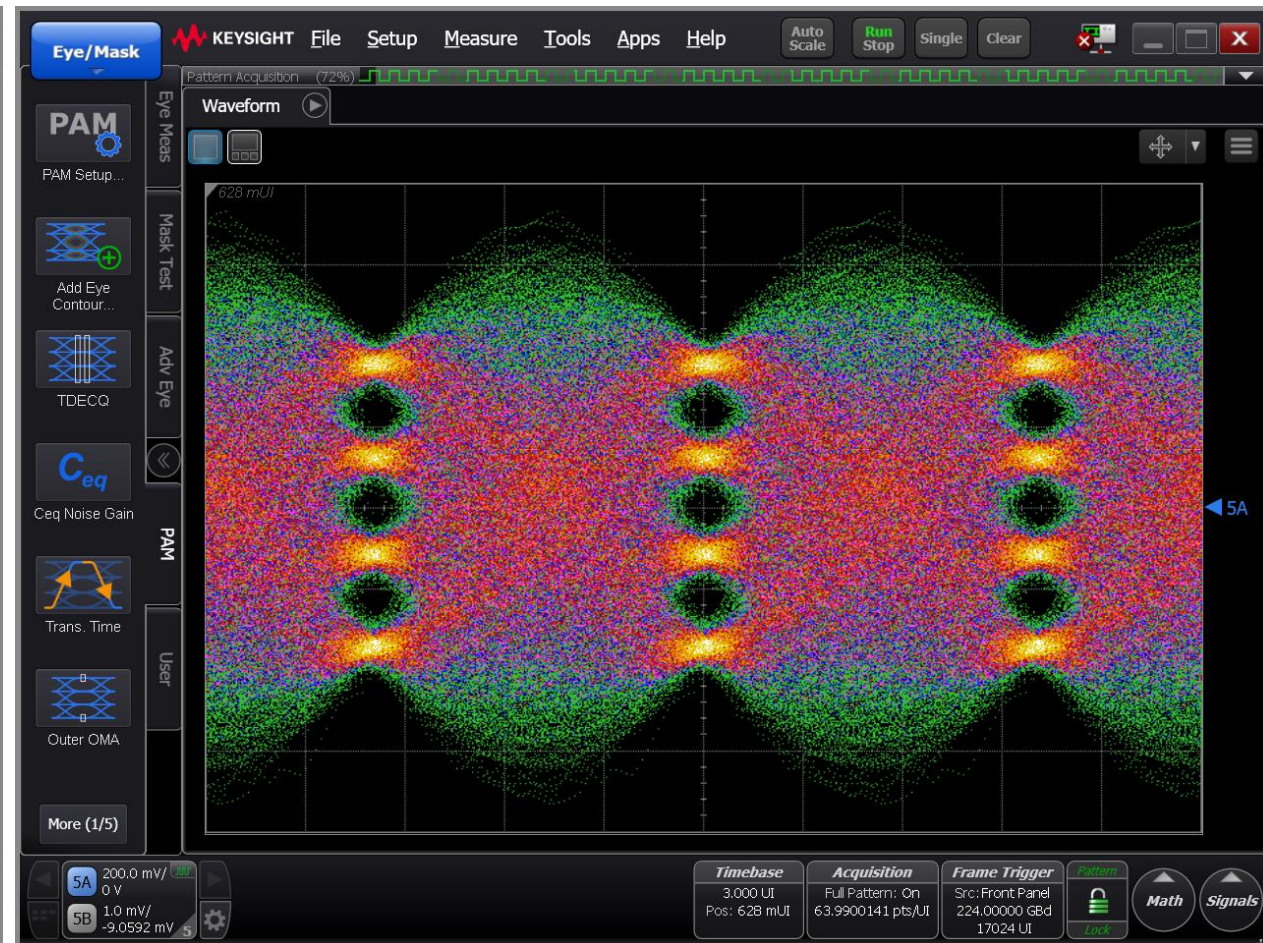
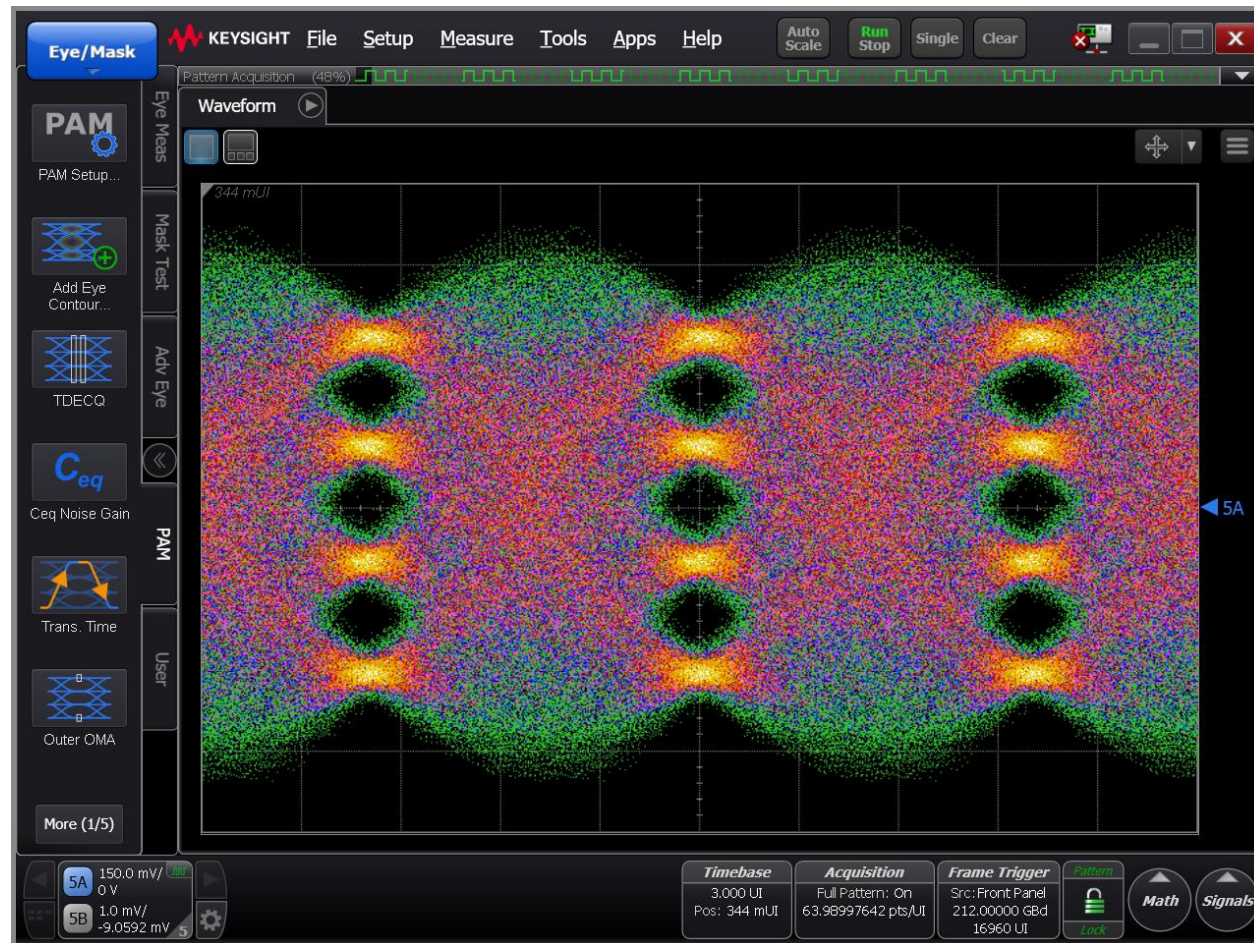


M8199B AWG + M8159A FDIU + External Amplifier

Sample measurements

200 Gbaud PAM4

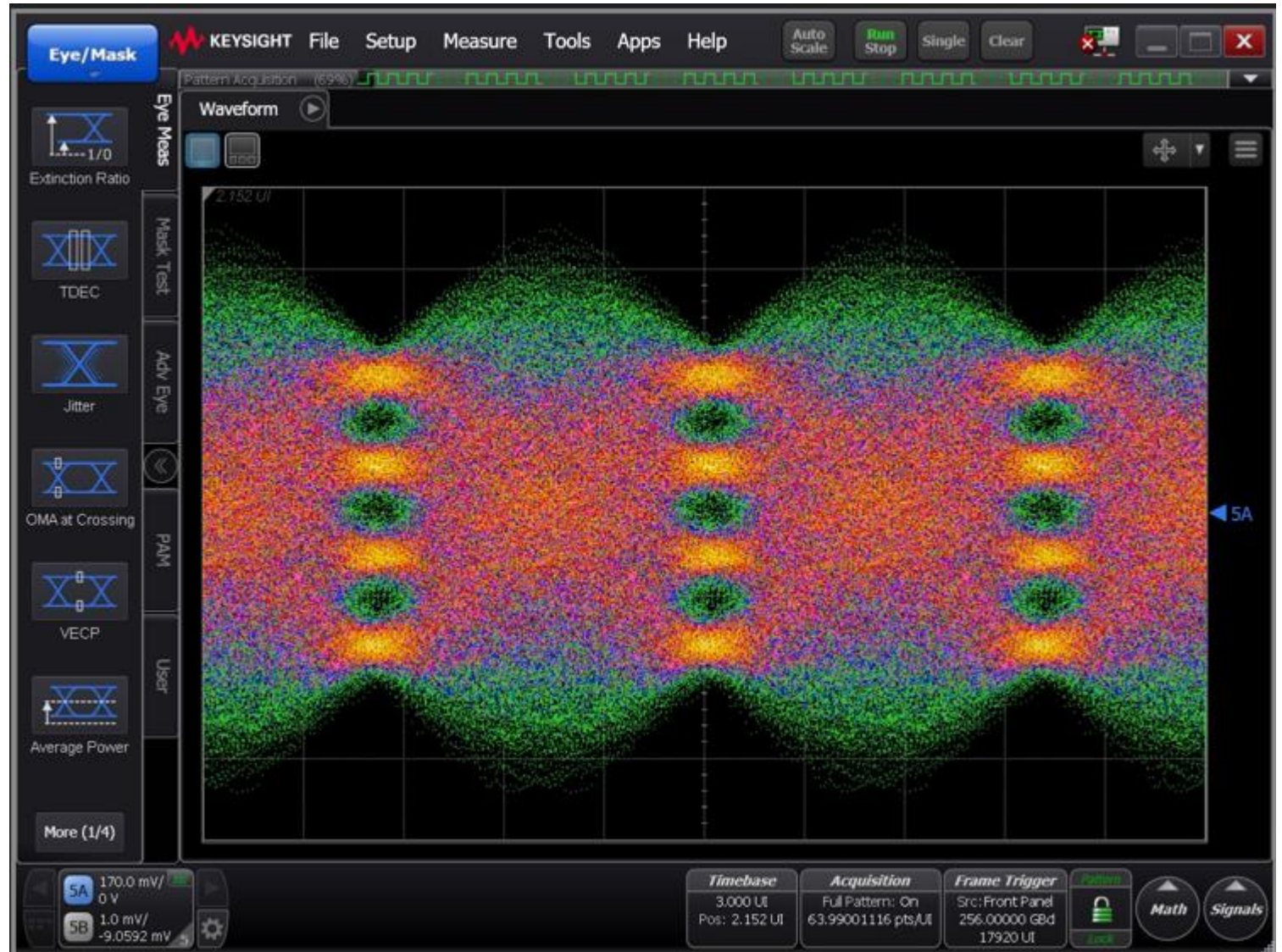
224 Gbaud PAM4



Anything worth doing is worth over-doing



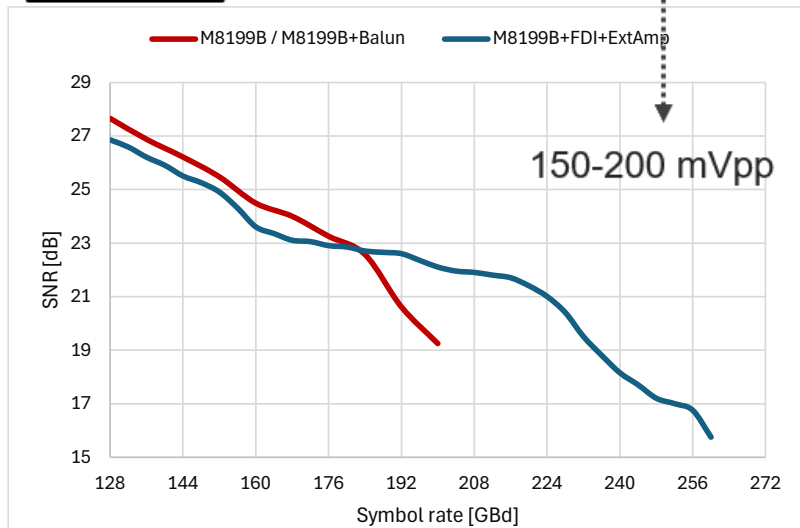
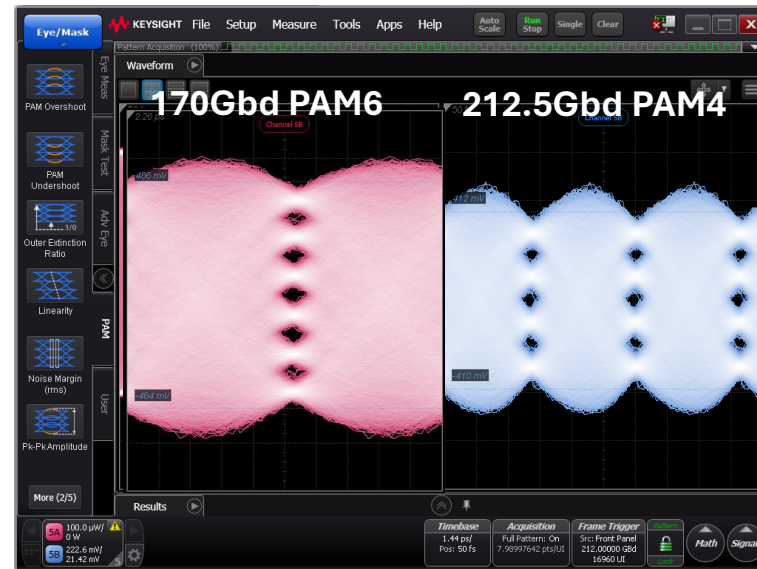
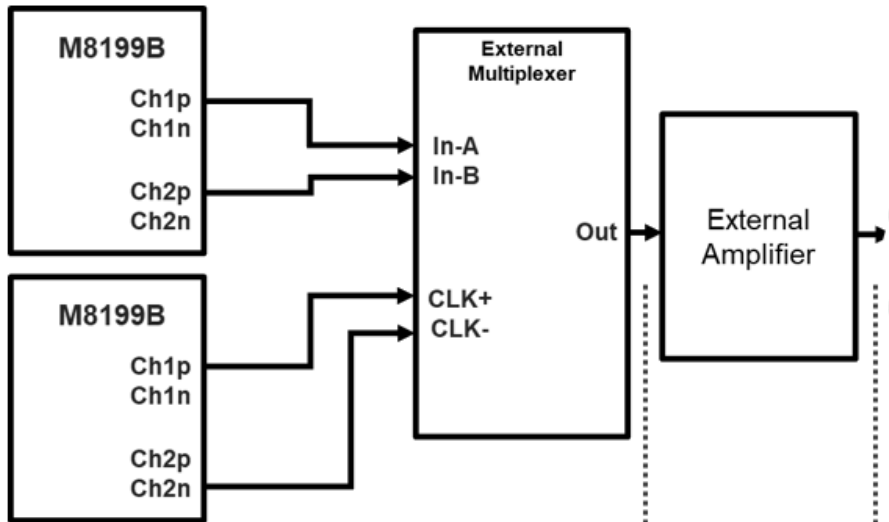
- Likely the fastest electrical signal ever recorded at 512Gbps PAM4.



Summary

- The 448Gbps design problem is well within the operational limits of contemporary signal generation and signal acquisition instruments today. New instrumentation and measurement science is being developed today to serve this rapidly approaching need.
- Bandwidth of channels/connectors is likely to determine the preference of modulation order.
 - Optics is not BW limited
 - Electrical channels are very limited.
- PAM8 at 150 GBd or PAM6 at 174Gbd satisfies many of the current channel limitations, however the fractional symbol rate (PAM6) will likely break KP4 FEC models.
- Compound use of FFE/DFE in series with MLSD shows almost an order of magnitude improvement in reducing BER, and curiously works best on sub nyquist channels.
- PAM4 practicality requires real 40% improvement to current connector technology. The 90GHz prominent resonance needs to be pushed up to 110GHz at minimum.
- Additional equipment details in backup slides.

Backup: Instrumentation details



700 mVpp
SNR ≥ 20 dB

425Gbs

- 400Gbps net rate
- 6.5% KP4 FEC overhead
- FEC threshold 2.2e-4

N1046A 100 GHz Electrical Remote Sampling

1, 2 and 4 channel configuration options

Head Module



Item	
Bandwidth ¹ , 3 dB (user selectable)	Options 11F, 12F, and 14F
100 GHz	•
<i>122 GHz (Characteristic)</i>	•
Transition Time (10% to 90% calculated from $t_r = 0.35/BW$)	Options 11F, 12F, and 14F
100 GHz	3.5 ps
<i>122 GHz (Characteristic)</i>	< 3.2 ps
RMS Noise	Options 11F, 12F, and 14F
100 GHz	1400 μ V <i>1050 μV (Characteristic)</i>
<i>122 GHz (Characteristic)</i>	<i>2000 μV (Characteristic)</i>

¹Fourth-Order Bessel-Thompson Response

Maximum specified: 100 GHz

Maximum characteristic: 122 GHz

Maximum SIRC² BW setting: 130 GHz

²System Impulse Response Correction

N1046A Frequency Response

Data from ONE module – not specification, but informative

Keysight N1046A(Slot1) Setup

Scale: 41.9 mV/

Bandwidth (4th order Bessel): SIRC

Offset: 7.675 mV

130.0 GHz

1A

Color... External HW... Advanced... Copy From... DATA SIM



Channel 1A Advanced Setup

Sampler Bandwidth: 122 GHz

Horizontal Adjustment

Hardware Skew: 0 s

Software Delay: 0 s

Units: Volt

Signal Type: Auto Detect

NRZ

N1032A/B 120 GHz Optical Modules

N1032A Single Channel | N1032B Dual Channel

Item	Description	
Optical Channel Count	1 (N1032A) 2 (N1032B)	
Optical Channel Bandwidth, -3 dBo ^{1, 8}	Option 09U	Option 13U
BW (unfiltered)	60 GHz	60 GHz
	80 GHz	80 GHz
	90 GHz	90 GHz
		110 GHz <i>120 GHz (characteristic)</i>
RMS Noise (<i>Characteristic</i>) ⁷	1310 nm	1550 nm
Unfiltered 80 GHz (-3 dBo)	<i>65 μW</i>	<i>120 μW</i>
Unfiltered 90 GHz (-3 dBo)	<i>90 μW</i>	<i>155 μW</i>
Unfiltered 110 GHz (-3 dBo)	<i>130 μW</i>	<i>235 μW</i>
Unfiltered 120 GHz (-3 dBo)	<i>175 μW</i>	<i>300 μW</i>
RMS Noise (Maximum) ⁷	1310 nm	1550 nm
Unfiltered 80 GHz (-3 dBo)	80 μW	135 μW
Unfiltered 90 GHz (-3 dBo)	105 μW	165 μW
Unfiltered 110 GHz (-3 dBo)	150 μW	235 μW
Unfiltered 120 GHz (-3 dBo)	185 μW	315 μW



N1032A Frequency Response

Data from ONE module – not specification, but informative



Reference Filter

SIRC Configure...

141.8182 GBd (106 GHz) Select From List

Wavelength

1310 nm

Channel 3A Advanced Setup ? Close

Sampler Bandwidth:

120 GHz (dBo) / 90.0 GHz (dBe)

Horizontal Adjustment

Hardware Skew:

0 s

Software Delay:

0 s

Units

Watt

Signal Type

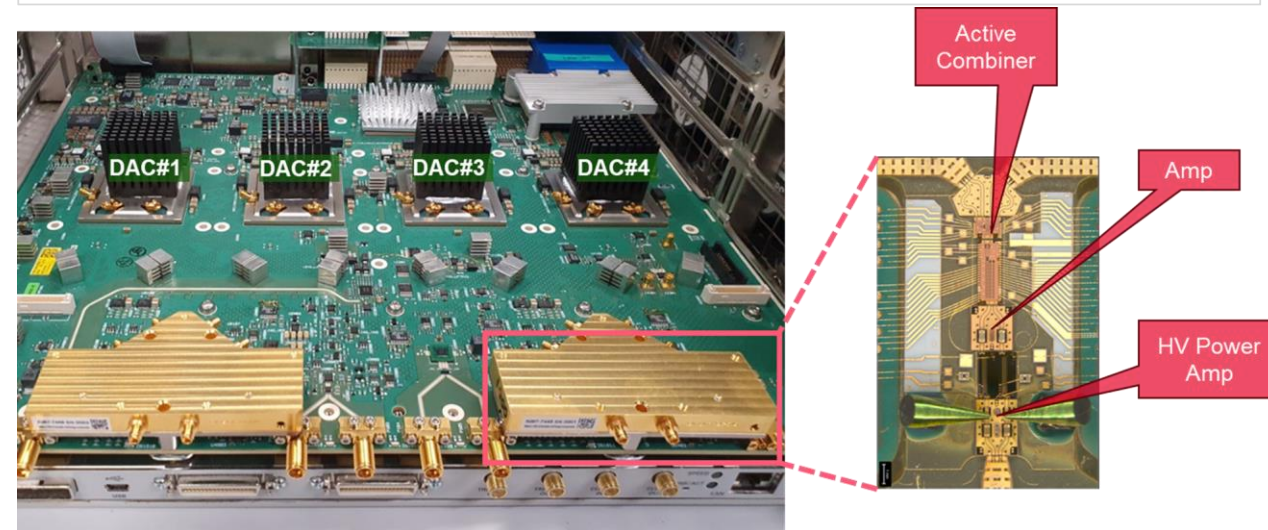
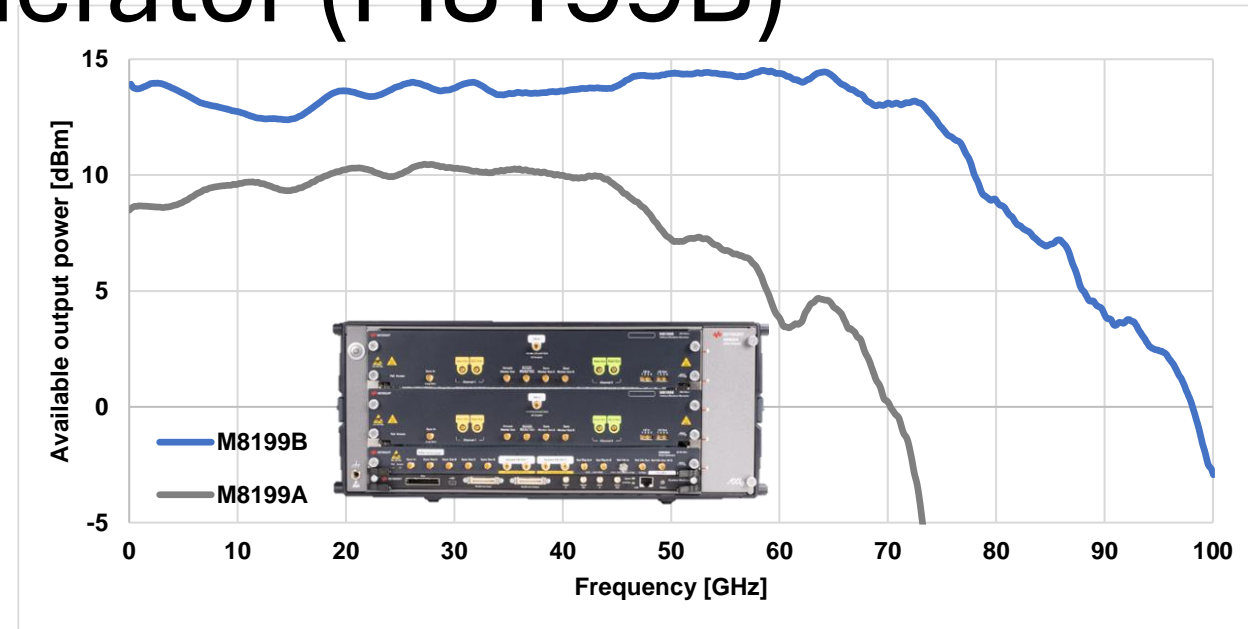
Auto Detect

Unspecified

Arbitrary Waveform Generator (M8199B)

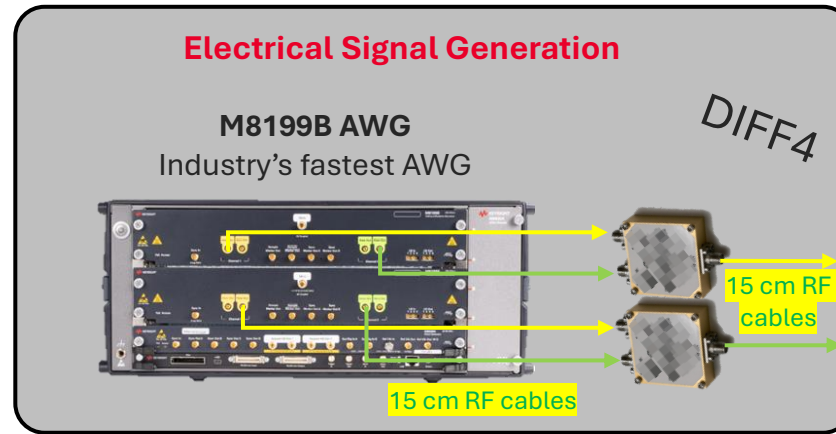
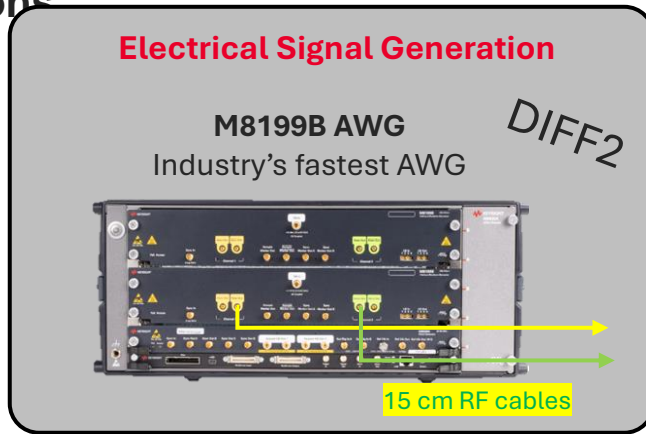
The AWG for development of 160 GBaud designs

- Continuous sample rate from 200 to 256 GSa/s
- 1 MSa of waveform memory per channel
- Analog bandwidth exceeding 80 GHz
- Synchronization of up to 8 channels across 4 modules
- Up to 2.2 Vpp differential output voltage at 160 Gbaud
- 5.0 Vpp amplitude at 400 MHz
- Built-in frequency and phase response calibration for clean output signals
- The only AWG in the industry allowing to generate 200 Gbaud signal

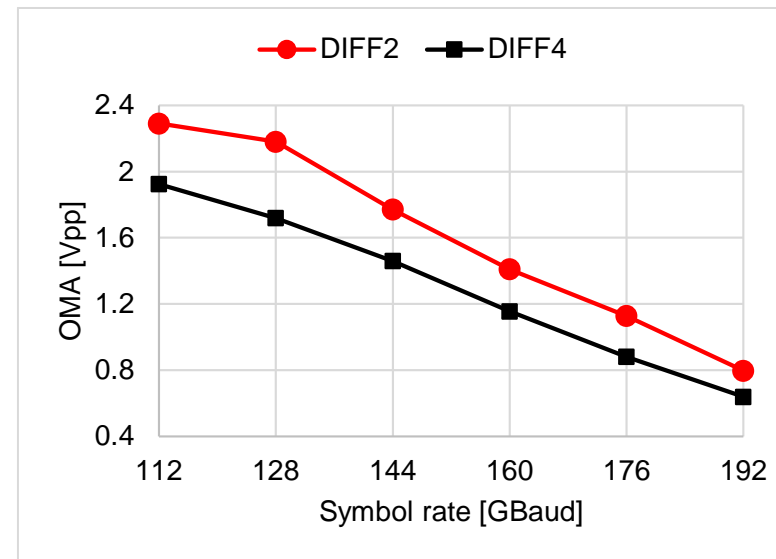
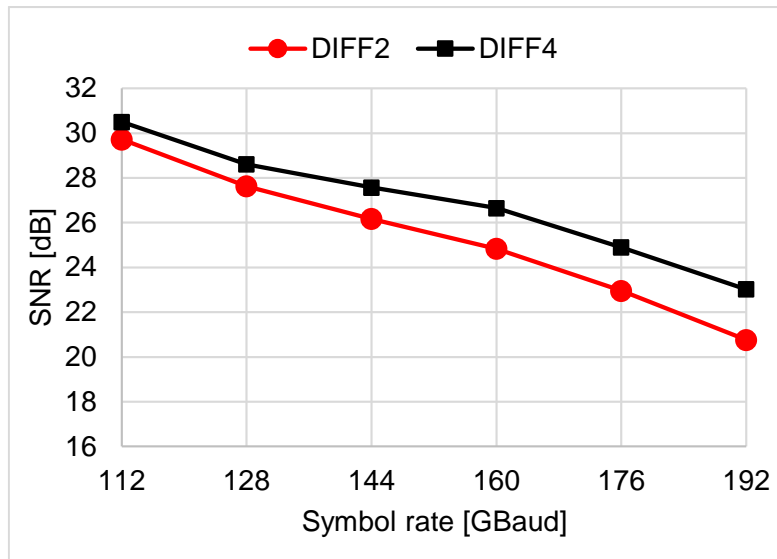
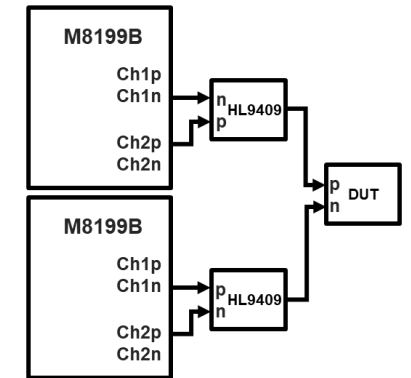


448 Gbps Electrical Measurements

Pathfinding Solutions



DIFF4: Multi-module virtual diff. channel



Differential signal generated with two M8199B AWGs (for each module, ch1p and ch2n connected to a high-bandwidth balun). Electrical signal measured with an N1046A DCA electrical channel module. A 10dB attenuator at each AWG output is used. Digital pre-distortion of AWG, 15 cm RF cables and balun applied at the transmitter side. No equalization is applied in FlexDCA.

448 Gbps Electrical BER Measurements

Pathfinding Solutions

KEYSIGHT REDUXR - M8070B

File Application System Clock Analyzer Patterns Measurements

Modules View x

Common

Channel 1

Data In

Parameters

- Acquisition: INF1.DataIn1
- Clock: INF1.DataIn1
- Symbol Rate: 150.00 GBd
- Line Coding: INF1.DataIn1
- Probe: INF1.DataIn1
- Comparator: INF1.DataIn1
- CDR: INF1.DataIn1
 - CDR State:
 - Loop Bandwidth: 12.500 MHz
 - Loop Bandwidth Divisor: 12000
 - Damping Factor: 0.700
- Input Timing: INF1.DataIn1
- Analyzer: INF1.DataIn1
- Alignment Results: INF1.DataIn1
- Equalization: INF1.DataIn1
 - State:
 - CTLE State:

Status Indicators

Module Channel Bit Rate Data Data L Sync L Stop Error Ratio

INF1 1 150.00 GBd 1...ols_PAM8 BER 1.42e-05

Clk Loss Global Outputs Enable Impairments Enable

Electrical Signal Generation

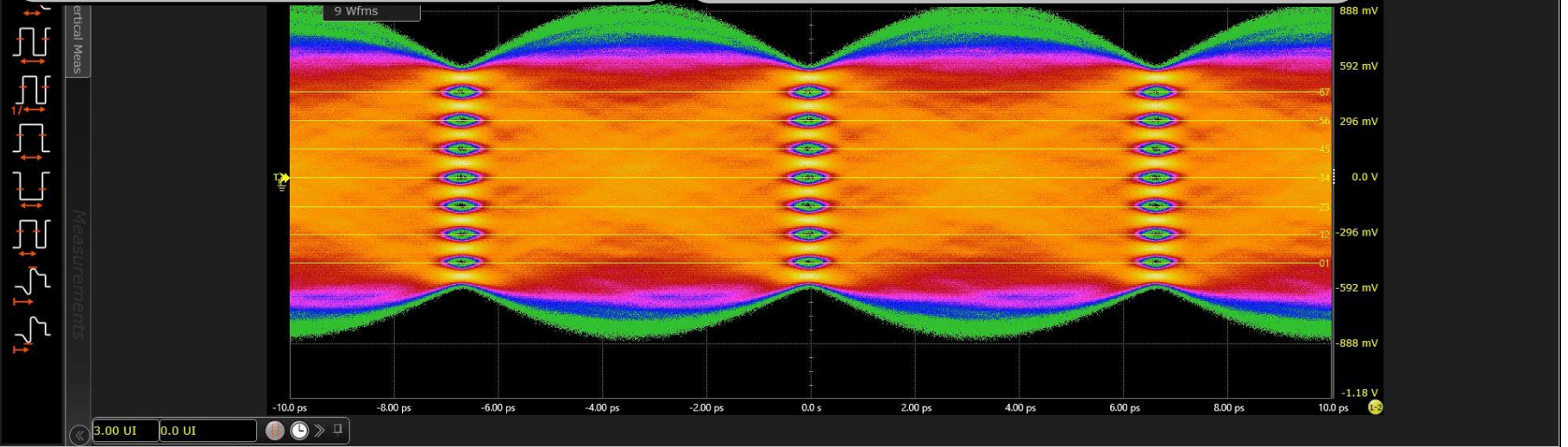
M8199B AWG
Industry's fastest AWG

15 cm RF cables

Electrical Signal Detection

Infiniium
UXR-Series
Oscilloscope
The world's most advanced real time oscilloscope.

30 cm RF cables



Results (Measure All Edges)

Measurement	Current	Mean	Min	Max	Range (Max-Min)	Std Dev	Count
Data length(1-2)	2.023478 Mb	2.023478 Mb	2.023478 Mb	2.023478 Mb	0 b	0 b	9

BER 1.42e-05

Remote Operations In Progress

GUI and front panel control has been disabled by remote operations. Press Enable if needed.

Enable


Differential signal generated with two M8199B AWGs. Electrical BER measured with a 110 GHz UXR real-time oscilloscope. Digital pre-distortion of AWG, RF cables and balun applied at the transmitter side. No equalization is applied in Infiniium UXR software.

448 Gbps Electrical BER Measurements

Pathfinding Solutions


Electrical Signal Generation

M8199B AWG
Industry's fastest AWG



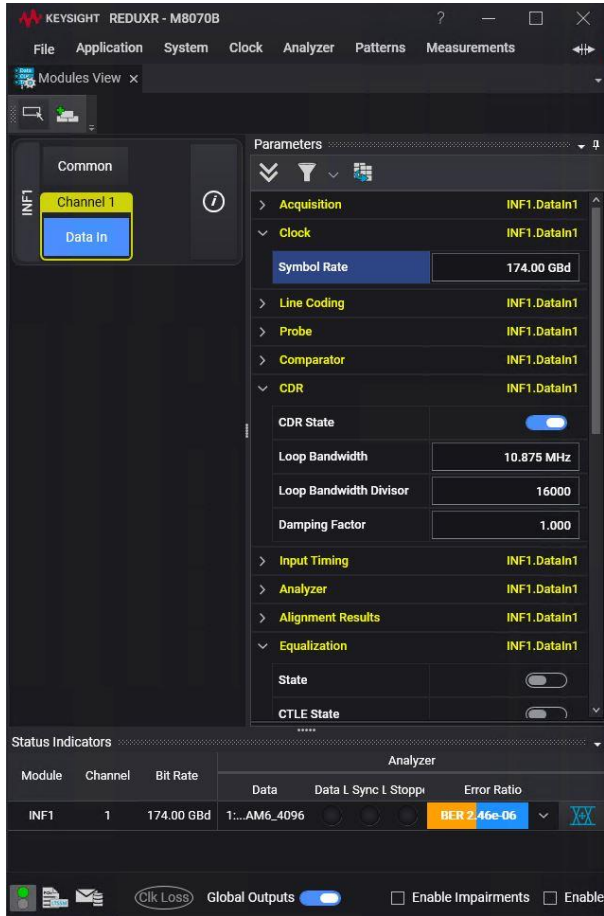
15 cm RF cables

Electrical Signal Detection



30 cm RF cables

Infiniium
UXR-Series
Oscilloscope
The world's most advanced real time oscilloscope.



KEYSIGHT REDUXR - M8070B

File Application System Clock Analyzer Patterns Measurements

Modules View x

Common

Channel 1

Data In

Parameters

- Acquisition: INF1.DataIn1
- Clock: INF1.DataIn1
- Symbol Rate: 174.00 GBd
- Line Coding: INF1.DataIn1
- Probe: INF1.DataIn1
- Comparator: INF1.DataIn1
- CDR: INF1.DataIn1
 - CDR State:
 - Loop Bandwidth: 10.875 MHz
 - Loop Bandwidth Divisor: 16000
 - Damping Factor: 1.000
- Input Timing: INF1.DataIn1
- Analyzer: INF1.DataIn1
- Alignment Results: INF1.DataIn1
- Equalization: INF1.DataIn1

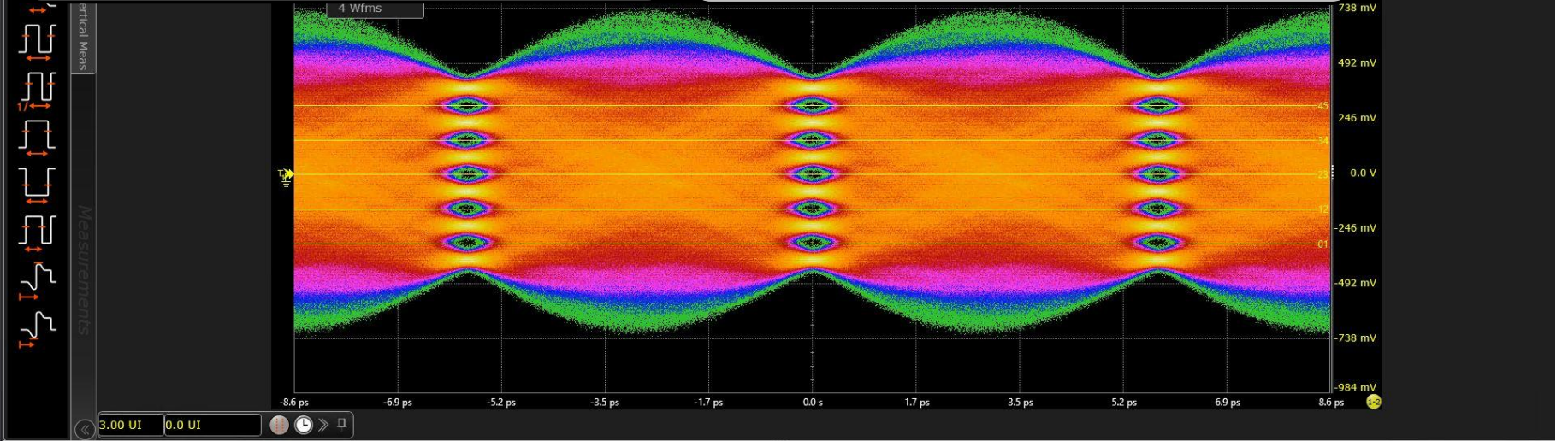
State:

CTLE State:

Status Indicators

Module	Channel	Bit Rate	Data	Data L Sync L Stoppi	Error Ratio
INF1	1	174.00 GBd	1...AM6_4096	<input type="checkbox"/>	BER 2.46e-06

Global Outputs: Clk Loss Enable Impairments Enable



Results (Measure All Edges)

Measurement	Current	Mean	Min	Max	Range (Max-Min)	Std Dev	Count
Data length(1-2)	2,031453 Mb	2,031447 Mb	2,031427 Mb	2,031455 Mb	28 b	13 b	4

Color Grade

- 1 - 7
- 8 - 14
- 15 - 20
- 30 - 59
- 60 - 118

Remote Operations In Progress

GUI and front panel control has been disabled by remote operations.

Press Enable if needed: 37 - 472

Enable

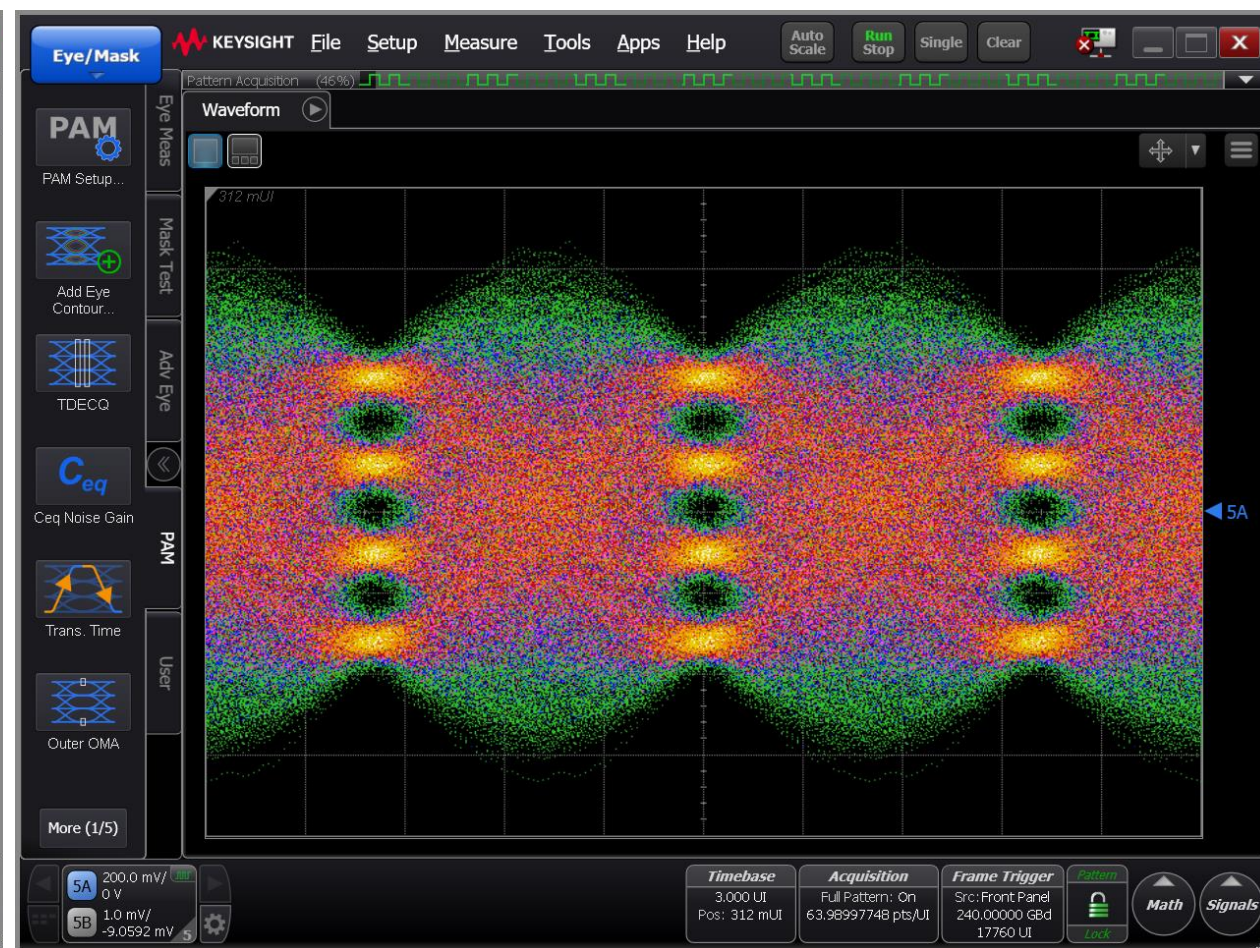
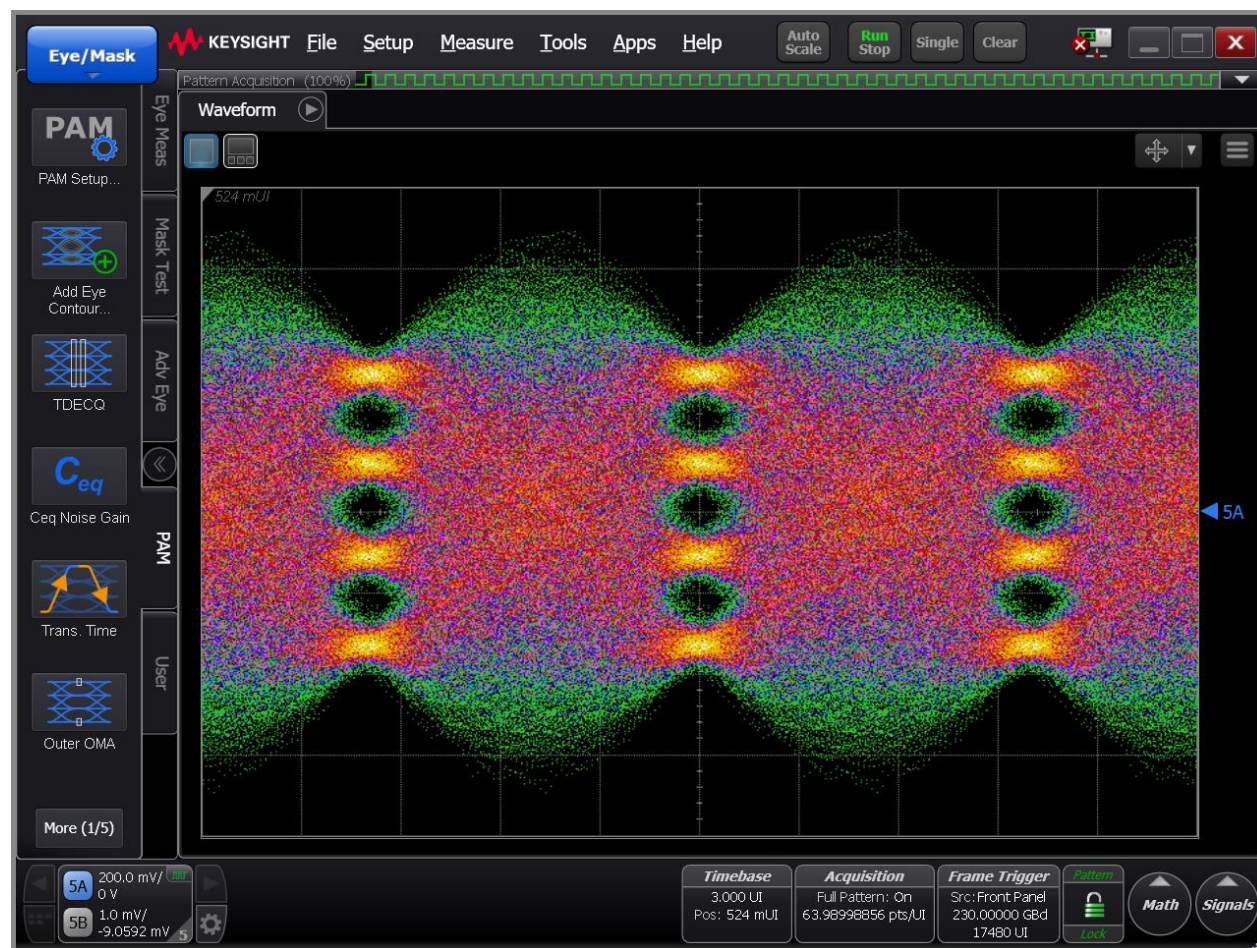
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M8199B AWG + M8159A FDIU + External Amplifier

Sample measurements

230 Gbaud PAM4

240 Gbaud PAM4



M8199B AWG + M8159A FDIU + External Amplifier

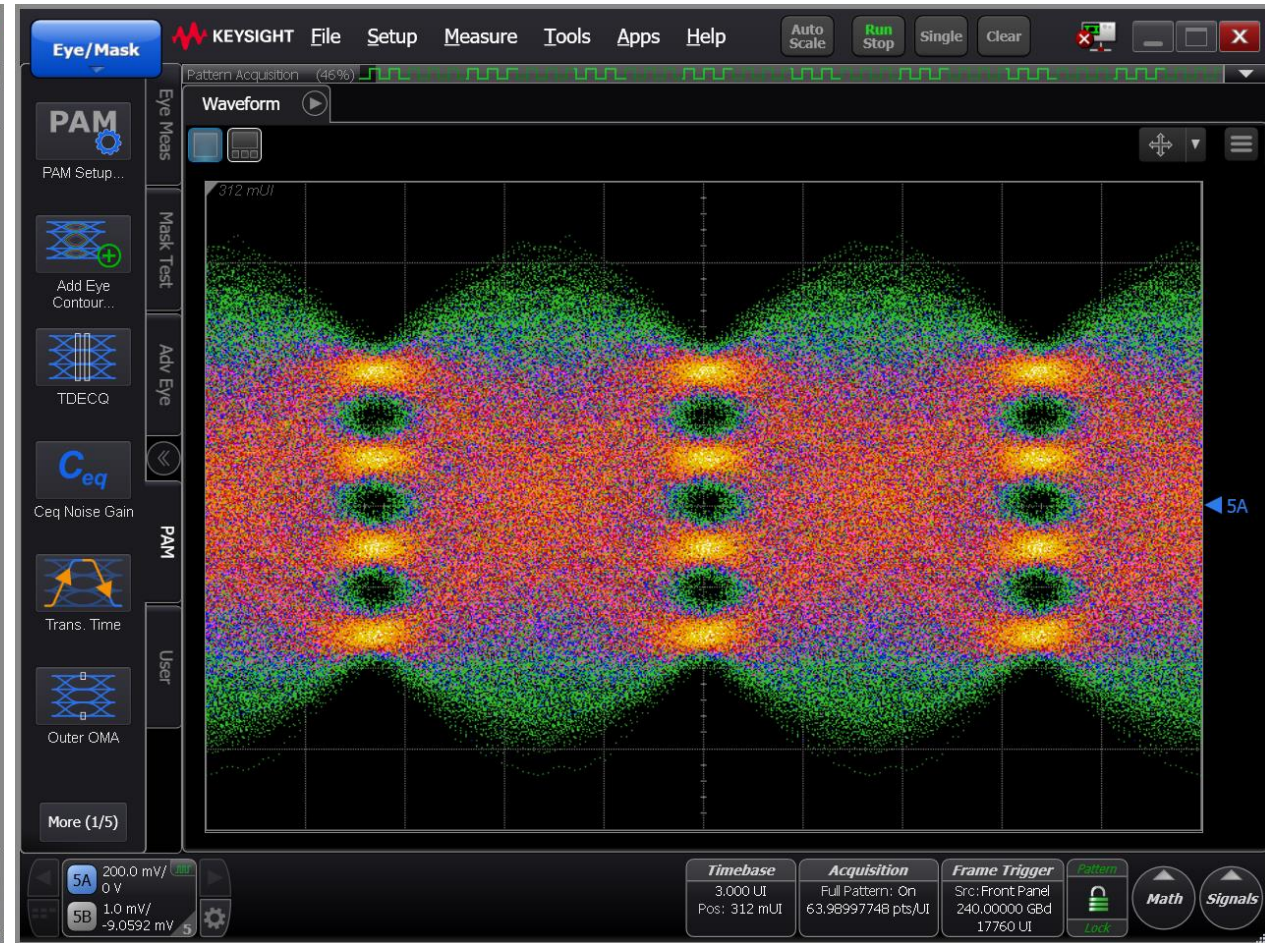
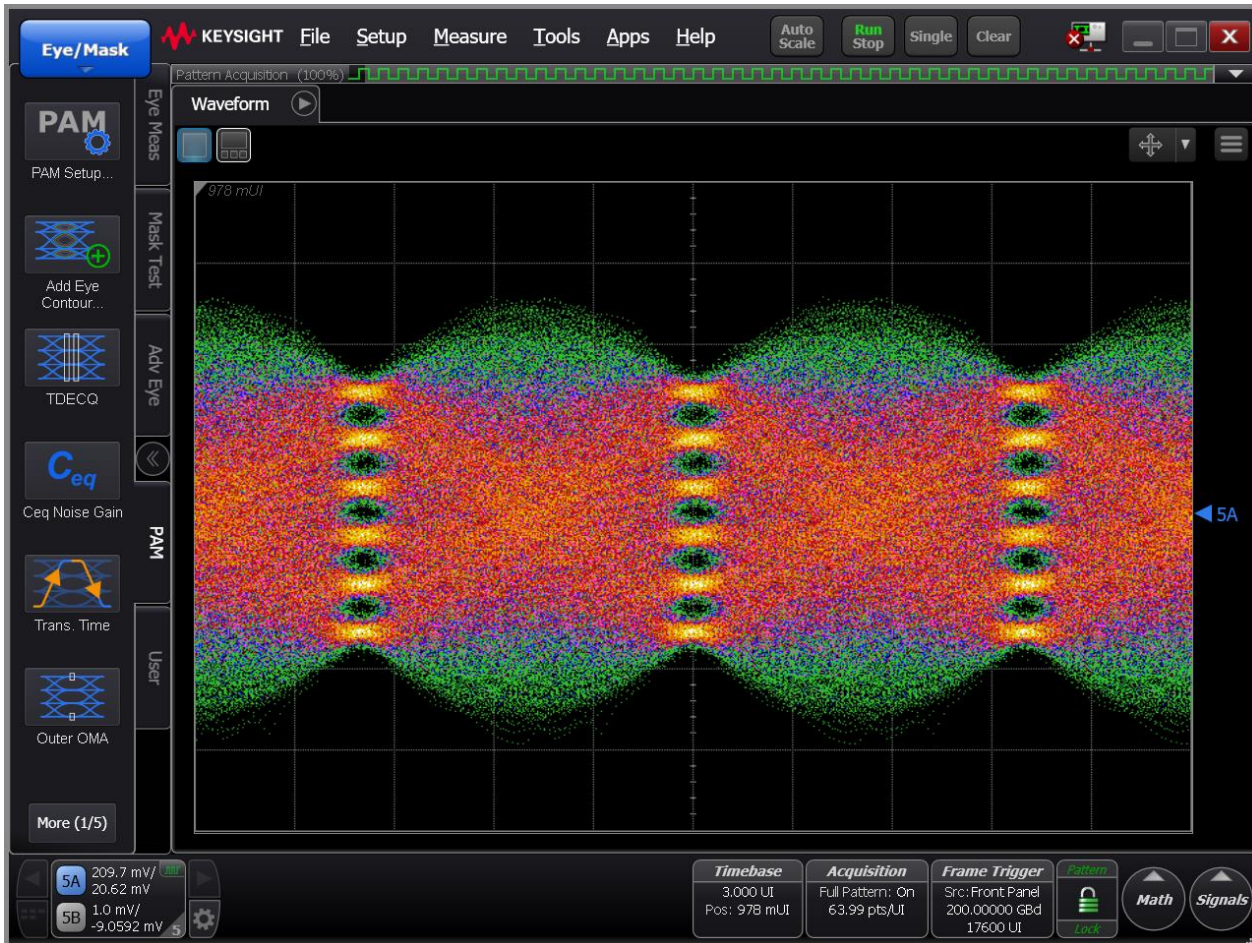
Sample measurements

Relevant for R&D
targeting 1600ZR



200 Gbaud PAM6 (tributary for 1.6T with DP-32QAM)

240 Gbaud PAM4 (tributary for 1.6T with DP-16QAM)



448 Gbps Optical Research

Demo at OFC25, Keysight booth 1301

