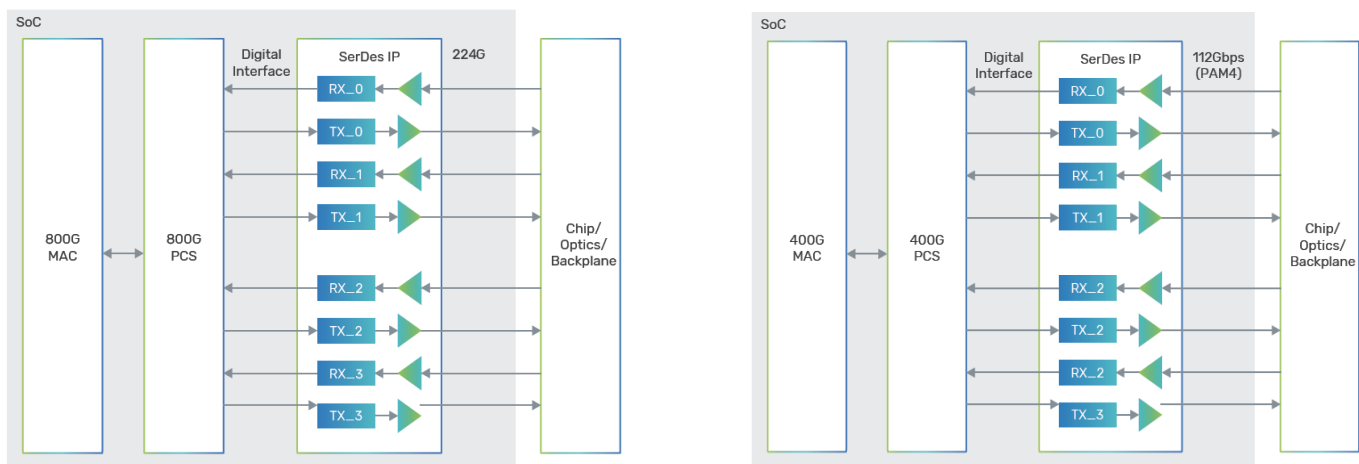# cadence®

Cadence Design Systems, Inc.

At ECOC 2023, Cadence is participating in demonstrations showcasing CEI-224G-LR, CEI-224G-VSR and CEI-112G-LR interoperability.

Cadence 224G Long-Reach (LR) SerDes PHY IP enables the emerging 800G and 1.6T networks for next-generation hyperscale data center and artificial intelligence (AI) infrastructures. Incorporating DSP SerDes technology, the IP supports 1Gbps to 225Gbps data rates for LR, MR, and VSR channels. Cadence provides PHY and Controller integrated subsystems. Learn more at www.cadence.com/go/224g.

CEI-224G-LR Interoperability: Cadence and other OIF members are demonstrating CEI-224G-LR interoperability. Cadence's 224G-LR SerDes PHY transmits signals through other vendors' LR channels showing transmitter far-end data eye on oscilloscope.

CEI-224G-VSR Interoperability: Cadence and other OIF members are demonstrating CEI-224G-VSR interoperability. Cadence's 224G-LR SerDes PHY transmits signals to another vendor's SerDes through a VSR channel that is provided by multiple vendors.

Cadence 112G Long-Reach (LR) SerDes PHY IP enables reliable high-speed data transfer over backplane, direct-attached cable (DAC), chip-to-chip, and chip-to-module channels for cloud networking, AI/ML and 5G infrastructures. With extended long-reach capability, the IP supports 1Gbps to 112.5Gbps for LR, MR and VSR channels with additional performance margin. Cadence provides PHY and Controller integrated subsystems. Learn more at http://www.cadence.com/go/112g.

CEI-112G-LR Interoperability: Cadence and other OIF members are demonstrating CEI-112G-LR interoperability. Cadence's 112G SerDes PHY receives signals from transmitter of another vendor's SerDes through a LR channel that is provided by multiple vendors. Cadence receiver bit-error-rate (BER) passes CEI-112G-LR standard specifications.

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