

ML4067-112-18 Channel Emulation Board

The ML4067-112-18 Channel Emulation Board is a key tool for the novel equalization techniques required at 800G. Capable of a variety of carefully designed differential test traces, this passive test accessory emulates real-world environments with precise inter-symbol interference (ISI) to calibrate or stress test DSPs, modules, gearboxes, and a multitude of other systems



ML4064-MCB-112

MultiLane's OSFP Module Compliance Board, the ML4064-MCB-112 is ideal for transceiver, AOC, and DAC testing at 8x112G. CMIS aligned, the MCBs capabilities can be enhanced with the addition of the ML4066 adapter and analyzer combination.



ML4062-MCB-112-18

The ML4062-MCB-112-18 is a QSFP-DD Module Compliance Board for 8x112G designed for maximum efficiency when testing QSFP-DD transceivers and AOCs. Developed to address the OIF-CEI-112G-VSR-PAM4 specification, the ML4062-MCB-112-MXPM70 simulates an ideal environment for QSFP-DD transceiver module testing, characterization and manufacturing.



Nexus Analyzer

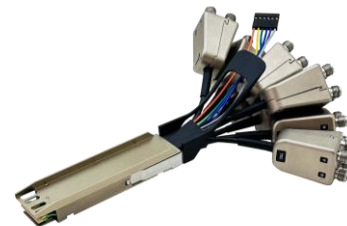
The latest interoperability tester from MultiLane, the Nexus Analyzer streamlines device interoperability and cuts down debugging times from hours to minutes with a suite of powerful tools that take some of the most time-consuming parts of CMIS/SFF validation and displaying them in one easy-to-read GUI: including CMIS/SFF register sweep, state machine and data path state machine testing, I2C R/W commands and packet analysis.



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OSFP800 ML4064-CHCB-112

The ML4064-CHCB-112 is a fully-featured OSFP 800G cabled Host Compliance Board (CHCB). Designed for IEEE802.3ck specification compliance, the CHCB provides a compact profile that allows for greater port testing density at 800G.



ML4079EN

The ML4079EN is a fully-featured 800G BERT with integrated jitter and noise insertion. Featuring real-hardware FEC, SJ, RJ, and BUJ injection, the ML4079EN is capable of automated and manual jitter and noise injection, with SJ injection up to 3x IEEE Mask specifications.



800G General Purpose BERT ML4079E

The ML4079E is a fully featured 800G BERT with real-hardware FEC that can be configured as an eight-channel PAM4 58 GBaud or eight-channel NRZ 28/58 Gbps lanes. The transmitters support all standard test patterns mandated by the OIF such as PRBS13Q, SSPRQ, PRBS31Q, etc. It is also possible to program the TX to output a user-defined pattern.



ABOUT MULTILANE

MultiLane is a leading provider of High-Speed IO and Data Center Interconnect test solutions. Products include BERTs, TDR, optical and electrical oscilloscopes, optical switch boxes, and a host of MSA-compliant development tools for QSFP28, QSFP-DD, OSFP, and other standards. MultiLane's products are used to test semiconductors, ACCs, DACs, AOCs, AECs, backplane cables, optical transceivers, and system switch cards.

