



## SPEAKING ENGAGEMENTS and SUPPORTED EVENTS

### DesignCon 2024 – OIF Panel Sessions

OIF Panel: [“Reining in Power Consumption Trends for Next Generation Optical Networking”](#)

Wednesday, January 31, 2024 – 12:15pm-1pm PT – Chiphead Theater

Moderator: Jeff Hutchins, OIF PLL WG EEI Vice Chair, Board Member, Ranovus

Panelists: Karl Bois, OIF Technical Committee Chair, Nvidia; Jeff Hutchins, OIF PLL WG EEI Vice Chair, Board Member, Ranovus; Yi Tang, OIF PLL WG Electrical Vice Chair, Cisco; Nathan Tracy, OIF President and Board Member, TE Connectivity

Abstract: The current trajectory of the portion of data center power allocated to networking is unsustainable for next generation data centers supporting a variety of applications including AI/ML. Join this session to hear OIF industry experts discuss OIF's newest project "Energy Efficient Interfaces Framework Project" which explores a variety of use cases as well as possible solutions to address this worrying trend. This complicated project is a joint effort across multiple OIF Physical & Link Layer Working Group tracks and the OIF's Physical Layer Users Group which is identifying the various applications to address.

Session Title: [“OIF Update on 224 Gbps Common Electrical Interfaces \(CEI\) Development”](#)

Wednesday, January 31, 2024 – 4pm-5:15pm PT – Ballroom F

Moderator: Nathan Tracy, OIF President, TE Connectivity

Panelists: John Calvin, Keysight Technologies; Mike Klempa, Alphawave Semi; Mike Li, OIF Board Member, Intel; Cathy Liu, OIF Vice President, Broadcom Inc.

Abstract:

Power efficient high-speed electrical interfaces will be required to enable next generation equipment and applications such as 800G and 1.6T OTNs based on 144 to 232 Gbps per-lane rates. A panel of OIF experts will present an update on the Common Electrical IO (CEI) developments that are work in-progress at OIF for these next generation architectures and applications. These experts will include lessons learned from 112 Gbps as well as some of the new challenges the industry will face at 224 Gbps. OIF is driving to enable power optimization for a range of applications by developing CEI specifications that are each unique to reach applications such as extremely short reaches for chip-to-chip & chip-to-optical engine (XSR), very short reaches such as chip-to-module (VSR), chip-to-chip for mezzanine & coplanar (MR), and long reaches for backplane and copper cable (LR). In addition, it is anticipated that there will be an industry demand for electrical interfaces optimized for latency to enable artificial intelligence (AI) and machine learning (ML) fabrics, networking, and computing. The optimizations to enable these conflicting requirements will be challenging for OIF member companies but will keep the industry moving forward with a new generation of interoperable electrical I/O interface specifications.

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## OFC 2024 – OIF Panels on the Show Floor

OIF Panel: “[OIF: Coherent Optics Unleashed: From 400ZR Success to 800ZR/LR Advancements and 1600ZR Kick-off](#)”

Wednesday, March 27, 2024- 4pm-5pm PST – Theater 1

Moderator: Karl Gass, OIF PLL WG Optical Vice Chair

Session Description:

OIF's 400G coherent specification (OIF-400ZR-02.0) has been a resounding success for interoperability. It's time for an update on OIF's next two generations of coherent specifications (800ZR, 800LR, and 1600ZR). In this session, a panel of OIF experts will give an update on 800ZR and 800LR technical work as well as an early status on 1600ZR, reviewing trade-offs in optical system performance, latency, power, and innovations. These projects will create the next core network architecture based on coherent pluggable technology common in next-generation Cloud DCI and Telecom edge network interconnects.

OIF Panel: “[Energy Efficient Interfaces – Reining in Power Consumption Trends for Next-Generation Optical Networking](#)”

Thursday, March 28, 2024 – 1:45pm–2:45pm PT – Theater 1

Moderator: Jeff Hutchins, OIF PLL WG EEI Vice Chair and Board Member, Ranovus

Panelists: Craig Thompson, Nvidia; Jeff Hutchins, OIF PLL WG EEI Vice Chair, Board Member, Ranovus; Yi Tang, OIF PLL WG Electrical Vice Chair, Cisco; Nathan Tracy, OIF President and Board Member, TE Connectivity

Session Description:

The current trajectory of the portion of data center power allocated to networking is unsustainable for next-generation data centers supporting a variety of applications, including AI/ML. Join this session to hear an OIF panel of industry experts discuss OIFs™ newest project, the Energy Efficient Interfaces Framework Project<sup>2</sup>, which explores a variety of use cases as well as possible solutions to address this worrying trend. This complicated project is a joint effort across multiple OIF working tracks including the OIF's Physical Layer Users Group which is identifying the various applications to address.